

IN THE CLAIMS:

1. (Previously Presented) An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in rows and columns in a matrix form;

gate signal lines, each of which is connected to and shared by a plurality of display pixels provided on each row;

gate drive circuits for sequentially supplying select signals to said gate signal lines;

a voltage source line is provided for each column; and

voltage from a voltage source is provided to each of said columns from only one end of said voltage source line, wherein

each of said display pixels includes an electroluminescence element, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to said select signal, and a second thin film transistor for driving said electroluminescence element based on said display signal; and

said gate drive circuits are placed so that said select signals are supplied from both ends of said gate signal lines to said gate signal lines, each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines.

2. (Original) An active matrix type electroluminescence display device according to claim 1, wherein said gate drive circuits include a first and second gate drive circuits arranged in a symmetric pattern to the right and left of a display region constructed from said plurality of display pixels.

3. (Original) An active matrix type electroluminescence display device according to claim 2, wherein each of said first and second gate drive circuits includes a plurality of shift registers for sequentially shifting a reference clock with a pulse width of one horizontal period.

4. (Original) An active matrix type electroluminescence display device according to claim 3, wherein each of said first and second gate drive circuits includes buffer amplifiers for driving said gate signal lines based on the output of said shift registers.

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5. (Original) An active matrix type electroluminescence display device according to claim 4, wherein the number of said shift registers and of the buffer amplifiers included in each of said first and second gate drive circuits corresponds to the number of rows of said plurality of display pixels.

6. (Previously Presented) An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in rows and columns in a matrix form;

gate signal lines, each of which is connected to and shared by a plurality of display pixels provided on each row;

gate drive circuits for sequentially supplying select signals to said gate signal lines;

a data line is provided for each column; and

a data signal is provided to each of said columns from only one end of said data line; wherein

each of said display pixels includes an electroluminescence element, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to said select signal, and a second thin film transistor for driving said electroluminescence element based on said display signal; and

said gate drive circuits are placed so that said select signals are supplied from both ends of said gate signal lines to said gate signal lines, each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines.

7. (Previously Presented) An active matrix type electroluminescence display device according to claim 6, wherein said gate drive circuits include a first and second gate drive circuits arranged in a symmetric pattern to the right and left of a display region constructed from said plurality of display pixels.

8. (Previously Presented) An active matrix type electroluminescence display device according to claim 7, wherein each of said first and second gate drive circuits includes a plurality of shift registers for sequentially shifting a reference clock with a pulse width of one horizontal period.

9. (Previously Presented) An active matrix type electroluminescence display device according to claim 8, wherein each of said first and second gate drive circuits includes buffer amplifiers for driving said gate signal lines based on the output of said shift registers.

10. (Previously Presented) An active matrix type electroluminescence display device according to claim 9, wherein the number of said shift registers and of the buffer amplifiers included in each of said first and second gate drive circuits corresponds to the number of rows of said plurality of display pixels.

11. (Previously Presented) An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in rows and columns in a matrix form;

gate signal lines, each of which is connected to and shared by a plurality of display pixels provided on each row;

gate drive circuits for sequentially supplying select signals to said gate signal lines;

a voltage source line and a data line are provided for each column; and

voltage from a voltage source is provided to each of said columns from only one end of said voltage source line and a data signal is provided to each of said columns from only one end of said data line; wherein

each of said display pixels includes an electroluminescence element, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to said select signal, and a second thin film transistor for driving said electroluminescence element based on said display signal; and

said gate drive circuits are placed so that said select signals are supplied from both ends of said gate signal lines to said gate signal lines, each of said gate signal lines is connected to said gate drive circuits at both ends of said gate signal lines.

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12. (Previously Presented) An active matrix type electroluminescence display device according to claim 11, wherein said gate drive circuits include a first and second gate drive circuits arranged in a symmetric pattern to the right and left of a display region constructed from said plurality of display pixels.

13. (Previously Presented) An active matrix type electroluminescence display device according to claim 12, wherein each of said first and second gate drive circuits includes a plurality of shift registers for sequentially shifting a reference clock with a pulse width of one horizontal period.

14. (Previously Presented) An active matrix type electroluminescence display device according to claim 13, wherein each of said first and second gate drive circuits includes buffer amplifiers for driving said gate signal lines based on the output of said shift registers.

15. (Previously Presented) An active matrix type electroluminescence display device according to claim 14, wherein the number of said shift registers and of the buffer amplifiers included in each of said first and second gate drive circuits corresponds to the number of rows of said plurality of display pixels.